

Model Name: GA-H61M-S2PT

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*1 SLOT
16	IT8892E
17	PCI SLOT1&2
18	ITE 8620 LPC IO
19	COM,KB_USB,USB_ESATA,-PROCHOT
20	HWM,FAN CTRL,OV,
21	DUAL BIOS
22	FP,FUSB,SPK,SATALED
23	Realtek ALC887-VD2
24	REAR AUDIO JACK
25	REALTEK RTL8111F-VL
26	DISCRETE POWER
27	ATX

SHEET TITLE

28	LINEAR CPU_VTT
29	VCORE ISL95836_1
30	VCORE ISL95836_2
31	VCORE ISL95836_3
32	LPT

Gigabyte Technology			
Title			
Cover Sheet			
Size	Document Number	Rev	
Custom	GA-H61M-S2PT	1.01	
Date:	Friday, June 20, 2014	Sheet	1 of 32

Revision 1.01

Component value change history

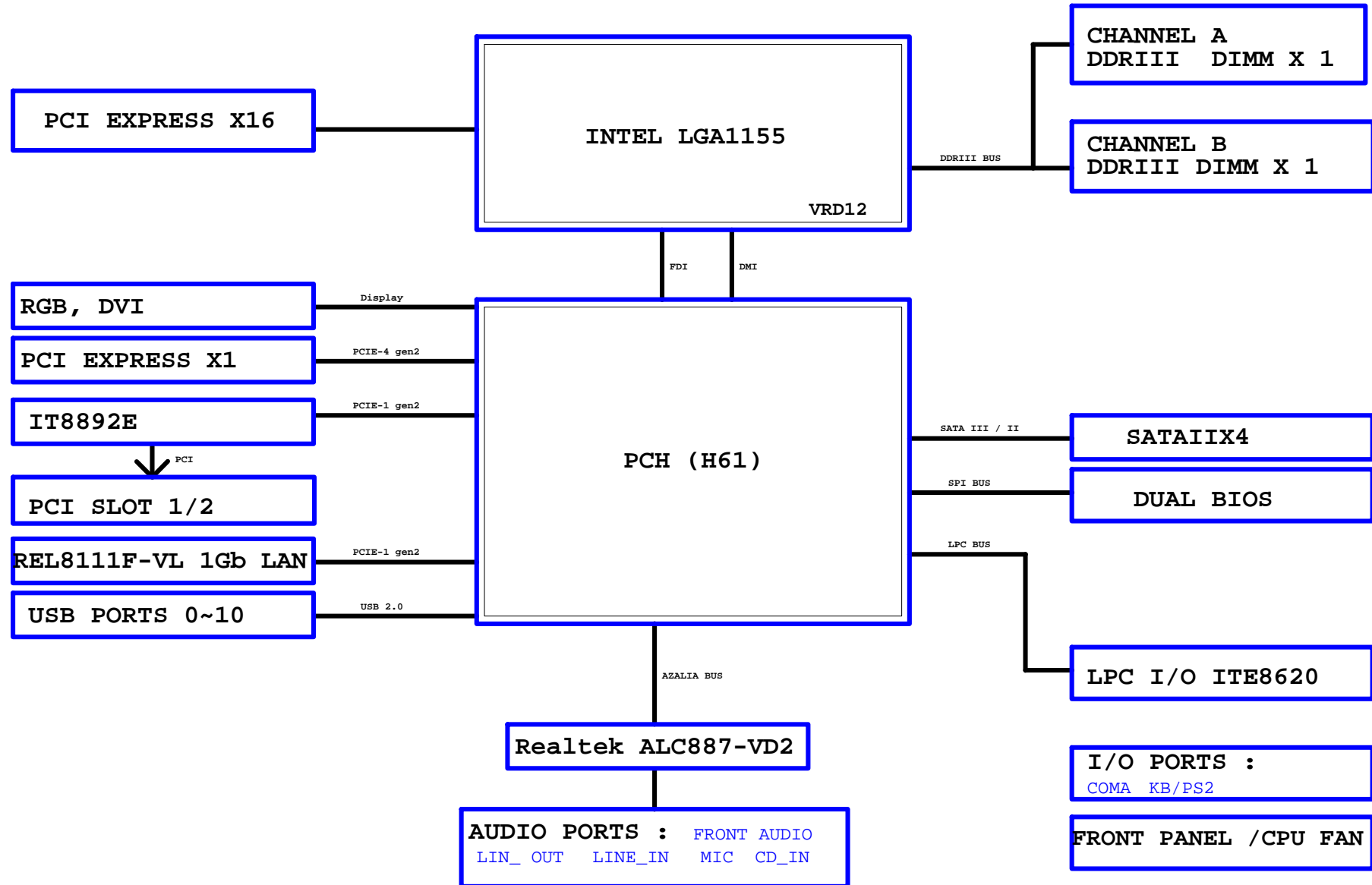
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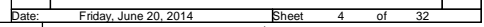
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Circuit or PCB layout change

[illegible]

BLOCK DIAGRAM





CPUA

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MAAA3	AW23	SA_MA[3]			
MAAA4	AV23	SA_MA[4]	SA_DQ[0]	AJ3	MDA0
MAAA5	AT24	SA_MA[5]	SA_DQ[1]	AJ4	MDA1
MAAA6	AT23	SA_MA[6]	SA_DQ[2]	AL3	MDA2
MAAA7	AU22	SA_MA[7]	SA_DQ[3]	AL4	MDA3
MAAA8	AV22	SA_MA[8]	SA_DQ[4]	AJ2	MDA4
MAAA9	AT22	SA_MA[9]	SA_DQ[5]	AJ1	MDA5
MAAA10	AV28	SA_MA[10]	SA_DQ[6]	AL2	MDA6
MAAA11	AU21	SA_MA[11]	SA_DQ[7]	AL1	MDA7
MAAA12	AT21	SA_MA[12]			
MAAA13	AW32	SA_MA[13]	SA_DQS[1]	AP3	DQSA1
MAAA14	AU20	SA_MA[14]	SA_DQS[1]	AP2	-DQSA1
MAAA15	AT20	SA_MA[15]			
[7] -SWEA	AW29	SA_WE#	SA_DQ[8]	AN1	MDA8
[7] -SCASA	AV30	SA_CAS#	SA_DQ[9]	AN4	MDA9
[7] -SRASA	AU28	SA_RAS#	SA_DQ[10]	AR3	MDA10
			SA_DQ[11]	AR4	MDA12
[7] SBAA0	AY29	SA_BS[0]	SA_DQ[12]	AN2	MDA11
[7] SBAA1	AW28	SA_BS[1]	SA_DQ[13]	AN3	MDA13
[7] SBAA2	AV20	SA_BS[2]	SA_DQ[14]	AR2	MDA14
			SA_DQ[15]	AR1	MDA15
[7] -CSA0	AU29	SA_CS#	SA_DQS[2]	AW4	DQSA2
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			SA_DQS[2]	AW4	DQSA2
			SA_DQS[2]	AW4	-DQSA2
[7] CKEA0	AV19	SA_CKE[0]	SA_DQ[16]	AV2	MDA16
[7] CKEA1	AT19	SA_CKE[1]	SA_DQ[17]	AW3	MDA17
			SA_DQ[18]	AV5	MDA18
			SA_DQ[19]	AW5	MDA19
			SA_DQ[20]	AU2	MDA20
			SA_DQ[21]	AU3	MDA21
			SA_DQ[22]	AU5	MDA22
			SA_DQ[23]	AU5	MDA23
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			SA_DQ[25]	AW8	-DQSA3
[7] DCLKA0	AY25	SA_CLK[0]	SA_DQ[26]	AY7	MDA24
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[7] DCLKA1	AU24	SA_CLK[1]	SA_DQ[28]	AV9	MDA26
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			SA_DQ[30]	AV7	MDA28
			SA_DQ[31]	AW7	MDA29
			SA_DQ[32]	AW9	MDA30
			SA_DQ[33]	AY9	MDA31
[7,8] -DDR3_RST	AW18	SM_DRAMRST#			
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			SA_DQ[32]	AU35	MDA32
			SA_DQ[33]	AW37	MDA33
			SA_DQ[34]	AU33	MDA34
			SA_DQ[35]	AU36	MDA35
			SA_DQ[36]	AW35	MDA36
			SA_DQ[37]	AY36	MDA37
			SA_DQ[38]	AU38	MDA38
			SA_DQ[39]	AU37	MDA39
			SA_DQS[5]	AP38	DQSA5
			SA_DQS[5]	AP39	-DQSA5
			SA_DQ[40]	AR40	MDA40
			SA_DQ[41]	AR37	MDA41
			SA_DQ[42]	AN38	MDA42
			SA_DQ[43]	AN37	MDA43
			SA_DQ[44]	AR39	MDA44
			SA_DQ[45]	AR38	MDA45
			SA_DQ[46]	AN39	MDA46
			SA_DQ[47]	AN40	MDA47
			SA_DQS[6]	AK38	DQSA6
			SA_DQS[6]	AK39	-DQSA6
			SA_DQ[48]	AL40	MDA48
			SA_DQ[49]	AL37	MDA49
			SA_DQ[50]	AJ38	MDA50
			SA_DQ[51]	AJ37	MDA51
			SA_DQ[52]	AL39	MDA52
			SA_DQ[53]	AL38	MDA53
			SA_DQ[54]	AJ39	MDA54
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			SA_DQ[56]	AG40	MDA56
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			SA_DQ[60]	AG39	MDA60
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DDR_0

1 OF 10

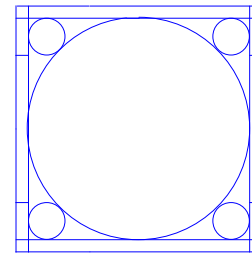
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MAAB3	AK18	SB_MA[3]			
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MAAB5	AP18	SB_MA[5]	SB_DQ[1]	AG8	MDB1
MAAB6	AM18	SB_MA[6]	SB_DQ[2]	AJ9	MDB2
MAAB7	AL18	SB_MA[7]	SB_DQ[3]	AJ8	MDB3
MAAB8	AY17	SB_MA[8]	SB_DQ[4]	AG5	MDB4
MAAB9	AN23	SB_MA[9]	SB_DQ[5]	AG6	MDB5
MAAB10	AN17	SB_MA[10]	SB_DQ[6]	AJ6	MDB6
MAAB11	AT18	SB_MA[11]	SB_DQ[7]	AJ7	MDB7
MAAB12	AR26	SB_MA[12]			
MAAB13	AY16	SB_MA[13]	SB_DQS[1]	AM8	DQSB1
MAAB14	AY16	SB_MA[14]	SB_DQS[1]	AL8	-DQSB1
MAAB15	AY16	SB_MA[15]			
[8] -SWEB	AR25	SB_WE#	SB_DQ[8]	AL7	MDB8
[8] -SCASB	AK25	SB_CAS#	SB_DQ[9]	AM7	MDB9
[8] -SRASB	AP24	SB_RAS#	SB_DQ[10]	AM10	MDB10
			SB_DQ[11]	AL10	MDB11
			SB_DQ[12]	AL9	MDB12
[8] SBAB0	AP23	SB_BS[0]	SB_DQ[13]	AM6	MDB13
[8] SBAB1	AM24	SB_BS[1]	SB_DQ[14]	AL9	MDB14
[8] SBAB2	AW17	SB_BS[2]	SB_DQ[15]	AM9	MDB15
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[8] -CSB0	AN25	SB_CS#	SB_DQS[2]	AP8	-DQSB2
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[8] CKEB0	AU16	SB_CKE[0]	SB_DQ[16]	AP7	MDB16
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			SB_DQ[20]	AP6	MDB20
			SB_DQ[21]	AR6	MDB21
			SB_DQ[22]	AP9	MDB22
			SB_DQ[23]	AR9	MDB23
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			SB_DQS[3]	AN12	-DQSB3
[8] DCLKB0	AL21	SB_CLK[0]	SB_DQ[24]	AM12	MDB24
[8] -DCLKB0	AL22	SB_CLK[0]	SB_DQ[25]	AM13	MDB25
[8] DCLKB1	AL20	SB_CLK[1]	SB_DQ[26]	AR13	MDB26
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			SB_DQ[28]	AL12	MDB28
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			SB_DQ[30]	AR12	MDB30
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			SB_DQ[42]	AP35	MDB42
			SB_DQ[43]	AP34	MDB43
			SB_DQ[44]	AR32	MDB44
			SB_DQ[45]	AR31	MDB45
			SB_DQ[46]	AR35	MDB46
			SB_DQ[47]	AR34	MDB47
			SB_DQS[6]	AL33	DQSB6
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			SB_DQ[50]	AL35	MDB50
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			SB_DQ[52]	AM34	MDB52
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			SB_DQ[54]	AM35	MDB54
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			SB_DQ[58]	AE34	MDB58
			SB_DQ[59]	AE35	MDB59
			SB_DQ[60]	AJ35	MDB60
			SB_DQ[61]	AJ34	MDB61
			SB_DQ[62]	AF33	MDB62
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CR
CPU RETENTION/X

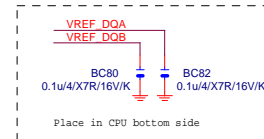
Need check the new CPU ME

CPU_P



ILM_BP/1156/CSP/ILM_BP/1156/CSP/12KRC-0F0001-06R_12KRC-0F0001-31R]

DDR SIGNAL



[7] MODT_A[0..1] <-- MODT_A[0..1]

[8] MODT_B[0..1] <-- MODT_B[0..1]

[7] MDA[0..63] <-- MDA[0..63]

[8] MDB[0..63] <-- MDB[0..63]

[7] DQSA[0..7] <-- DQSA[0..7]

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[7] MAA[A0..15] <-- MAA[A0..15]

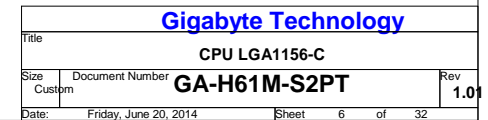
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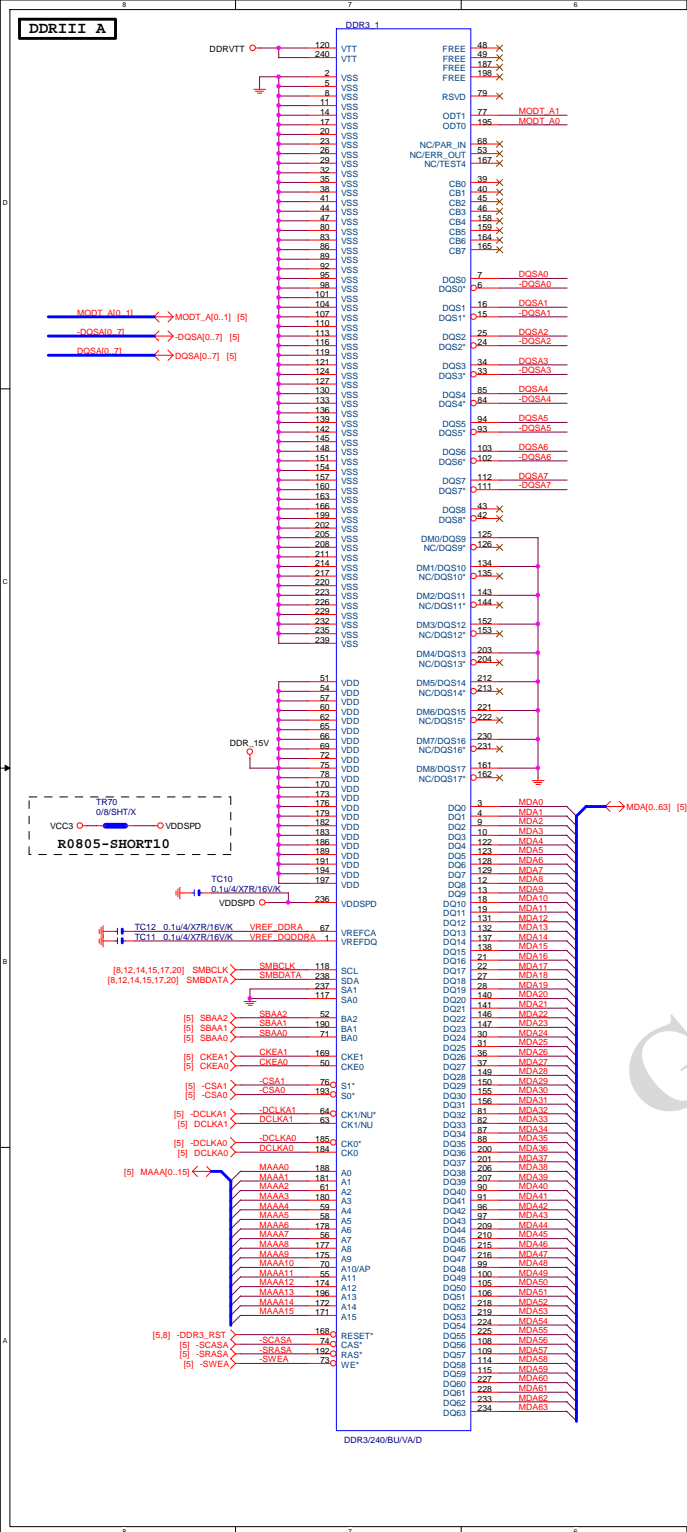
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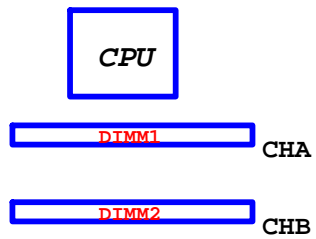
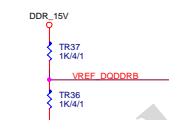
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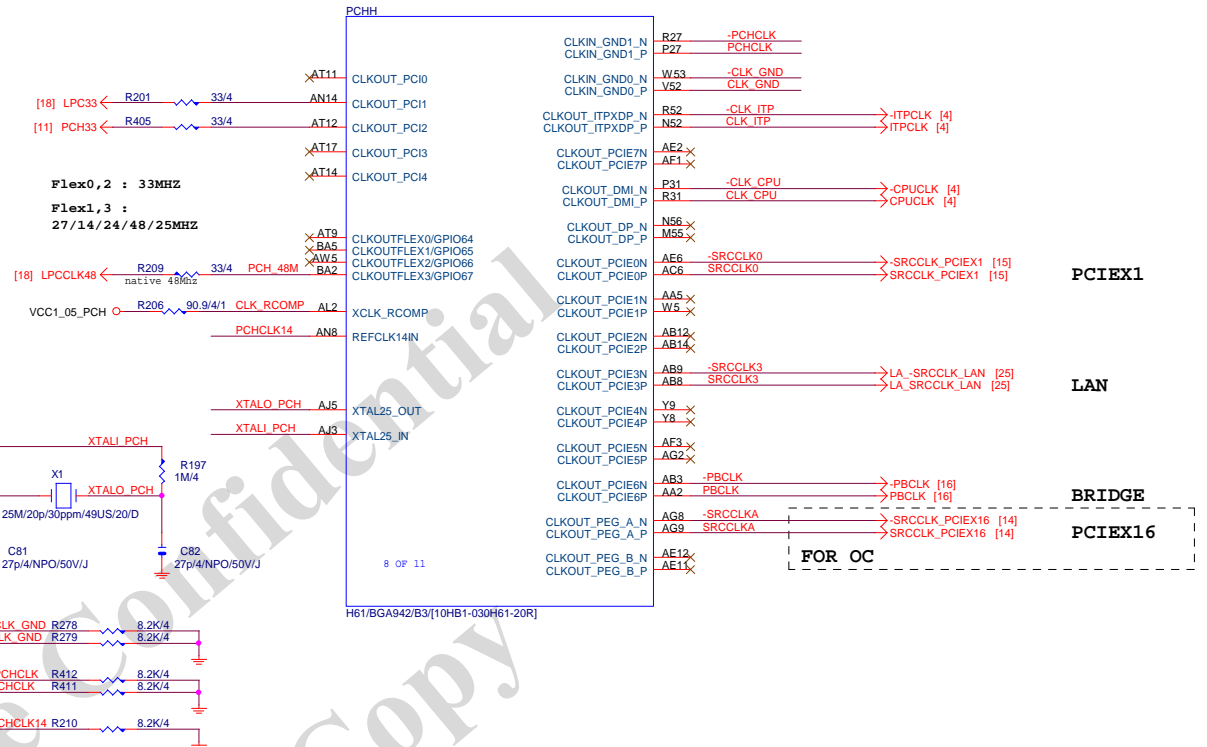
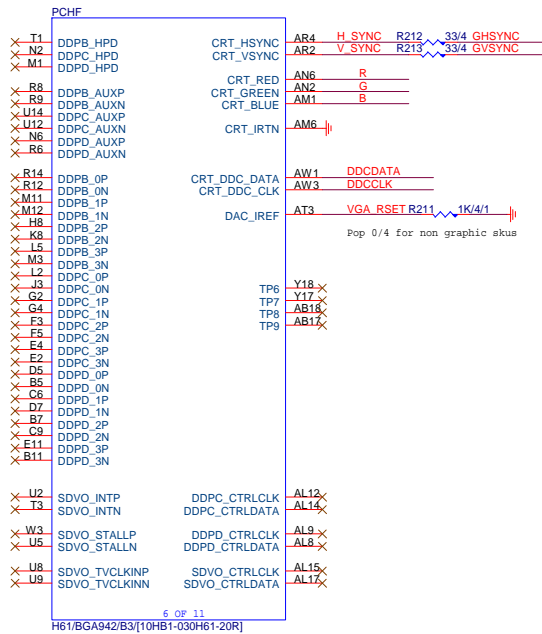
Gigabyte Technology

Title				CPU LGA1156-B	
Size				GA-H61M-S2PT	
Date:				Friday, June 20, 2014	Sheet 5 of 32

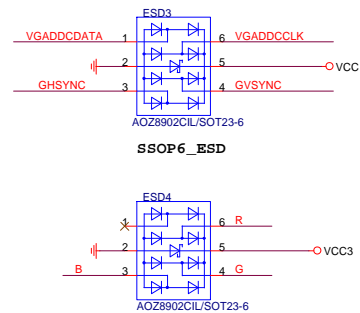




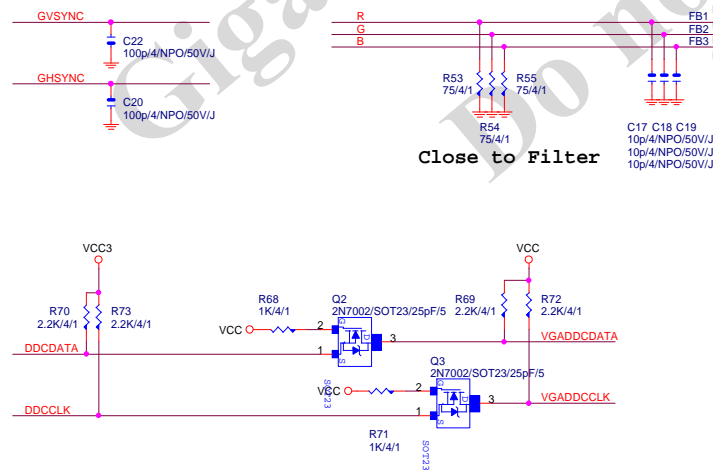




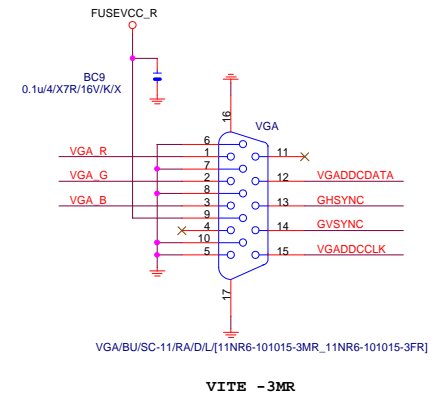
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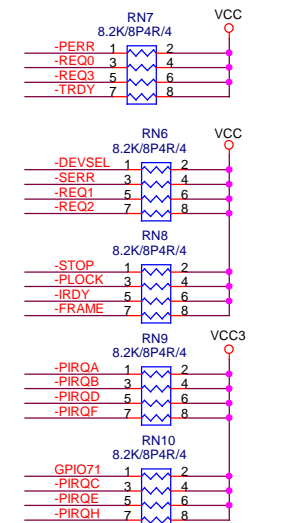
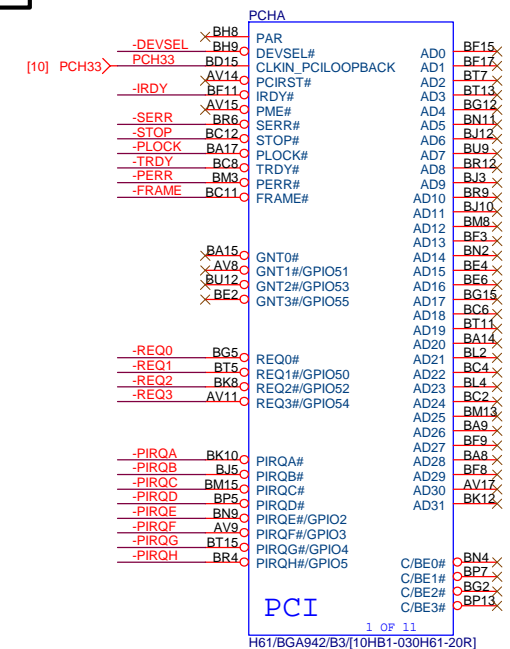


VGA SIGNAL



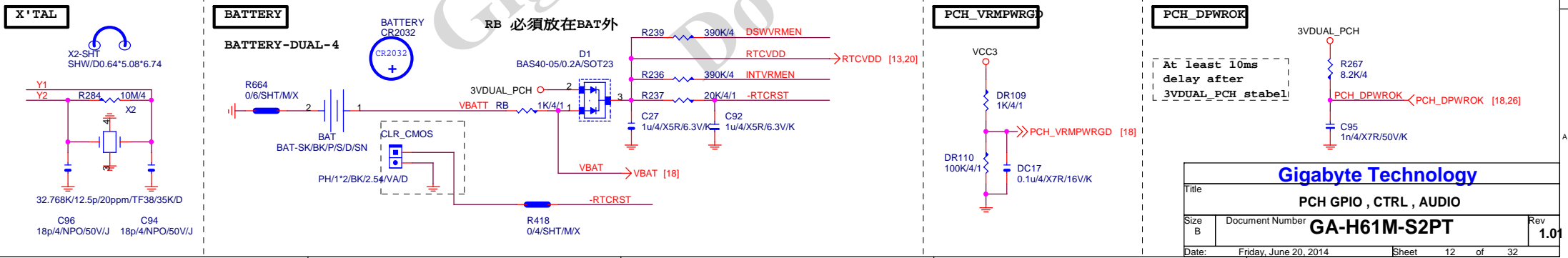
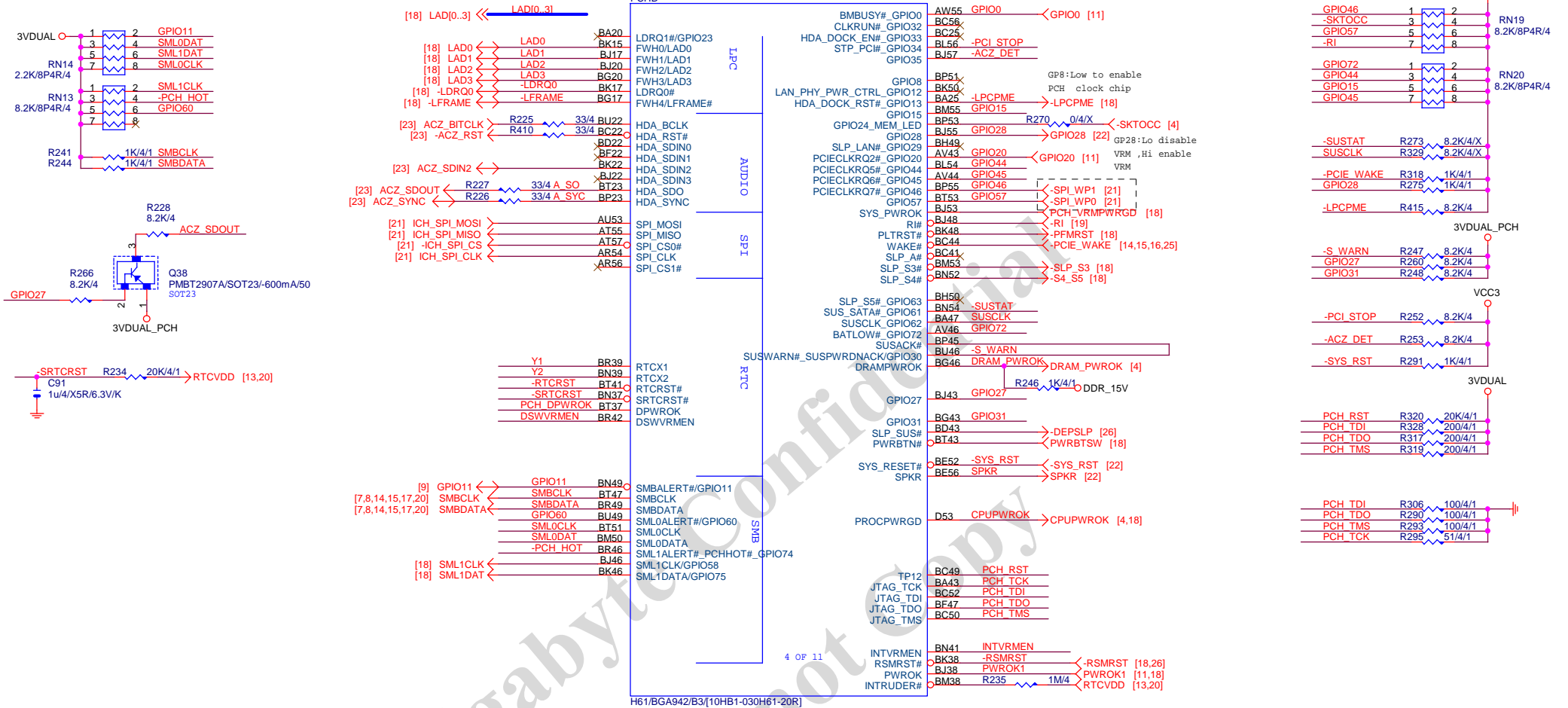
D-SUB



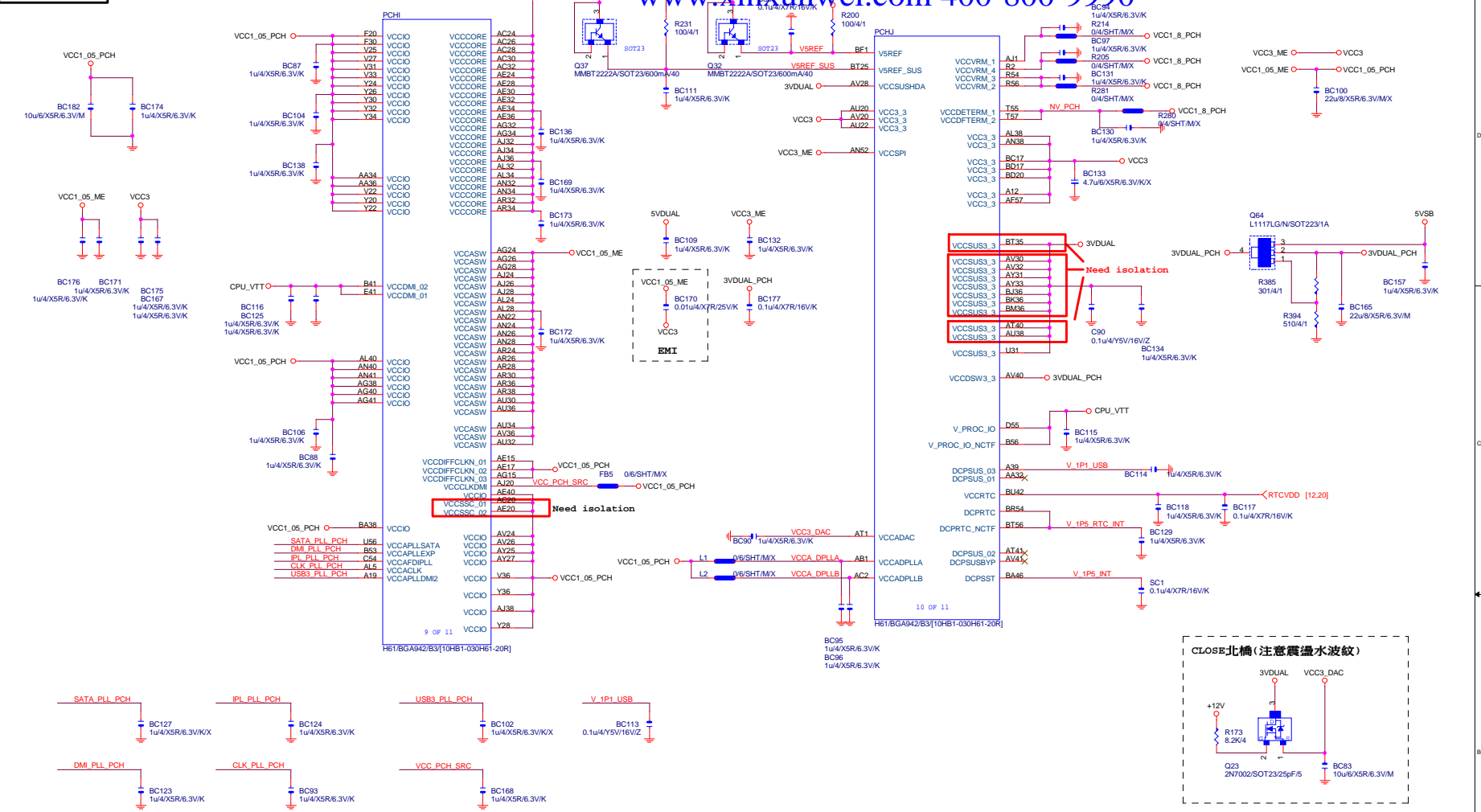


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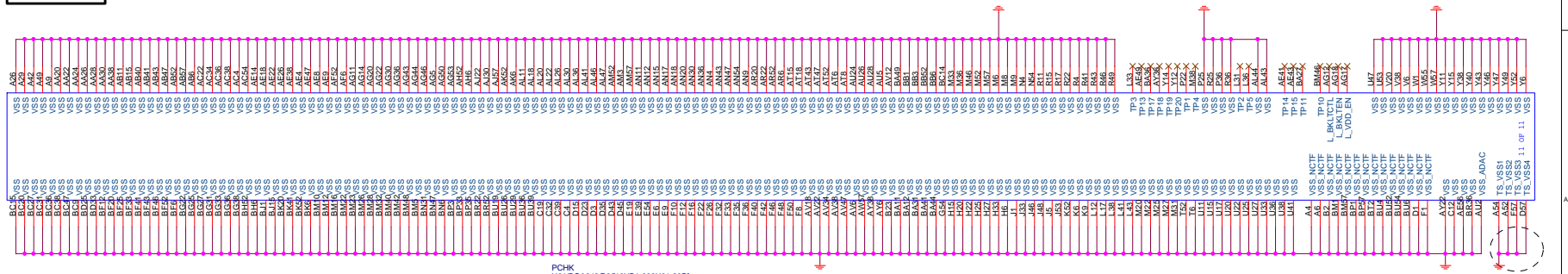
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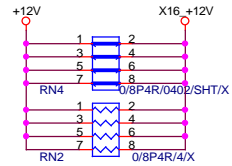
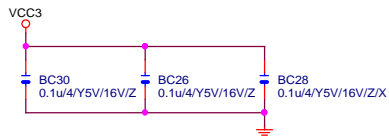
PCH I POWER



PCH K GND

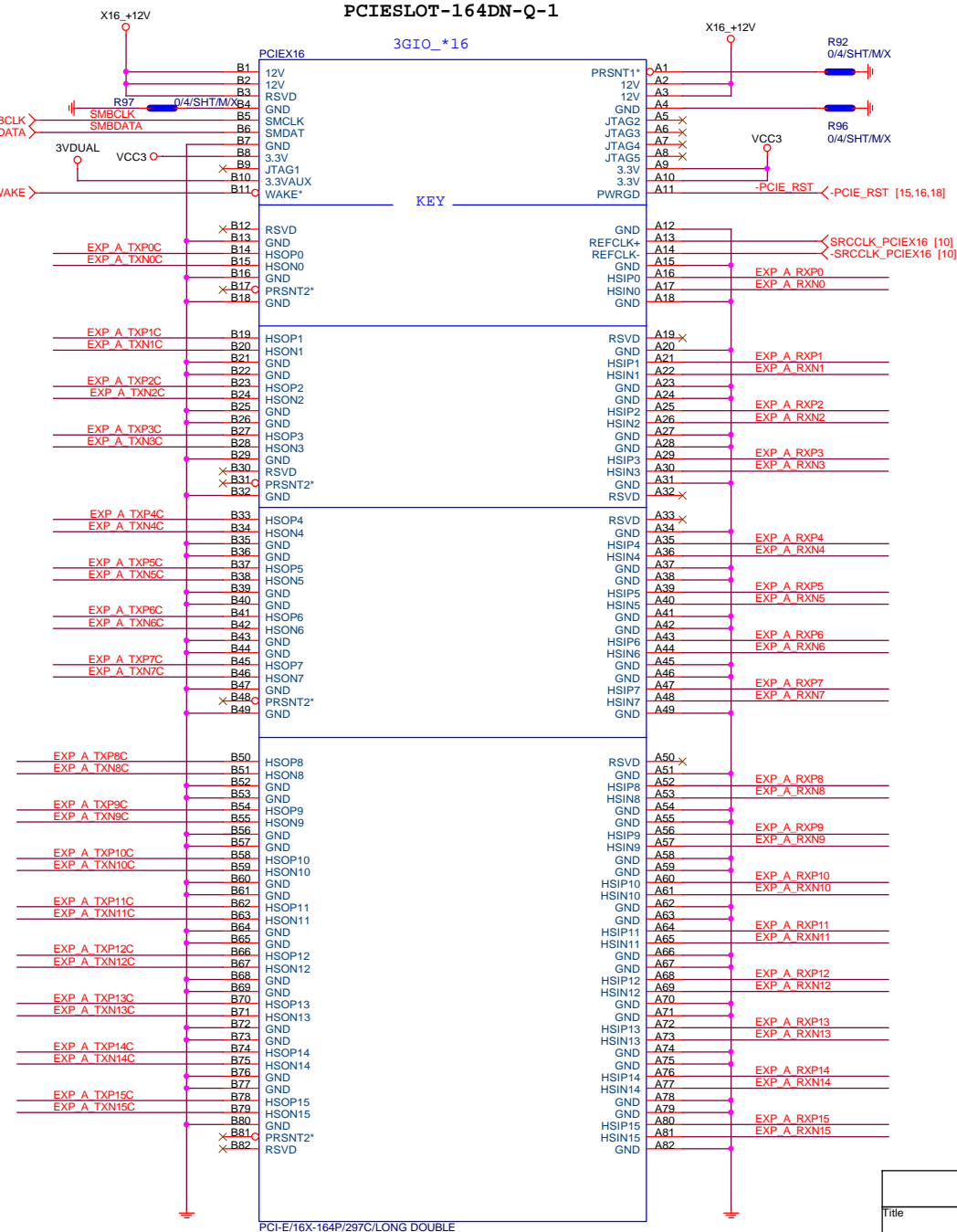


PCIEX16



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 EXP A RXN0..15] >> EXP_A_RXN[0..15] [4]
 EXP A TXP0..15] >> EXP_A_TXP[0..15] [4]
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EXP A TXN0	C30	0.22u/4/X5R/6.3V/K	EXP A TXN0C
EXP A TXP1	C35	0.22u/4/X5R/6.3V/K	EXP A TXP1C
EXP A TXN1	C37	0.22u/4/X5R/6.3V/K	EXP A TXN1C
EXP A TXP2	C39	0.22u/4/X5R/6.3V/K	EXP A TXP2C
EXP A TXN2	C41	0.22u/4/X5R/6.3V/K	EXP A TXN2C
EXP A TXP3	C43	0.22u/4/X5R/6.3V/K	EXP A TXP3C
EXP A TXN3	C45	0.22u/4/X5R/6.3V/K	EXP A TXN3C
EXP A TXP4	C46	0.22u/4/X5R/6.3V/K	EXP A TXP4C
EXP A TXN4	C49	0.22u/4/X5R/6.3V/K	EXP A TXN4C
EXP A TXP5	C50	0.22u/4/X5R/6.3V/K	EXP A TXP5C
EXP A TXN5	C51	0.22u/4/X5R/6.3V/K	EXP A TXN5C
EXP A TXP6	C52	0.22u/4/X5R/6.3V/K	EXP A TXP6C
EXP A TXN6	C54	0.22u/4/X5R/6.3V/K	EXP A TXN6C
EXP A TXP7	C57	0.22u/4/X5R/6.3V/K	EXP A TXP7C
EXP A TXN7	C58	0.22u/4/X5R/6.3V/K	EXP A TXN7C
EXP A TXP8	C60	0.22u/4/X5R/6.3V/K	EXP A TXP8C
EXP A TXN8	C61	0.22u/4/X5R/6.3V/K	EXP A TXN8C
EXP A TXP9	C62	0.22u/4/X5R/6.3V/K	EXP A TXP9C
EXP A TXN9	C63	0.22u/4/X5R/6.3V/K	EXP A TXN9C
EXP A TXP10	C64	0.22u/4/X5R/6.3V/K	EXP A TXP10C
EXP A TXN10	C65	0.22u/4/X5R/6.3V/K	EXP A TXN10C
EXP A TXP11	C66	0.22u/4/X5R/6.3V/K	EXP A TXP11C
EXP A TXN11	C67	0.22u/4/X5R/6.3V/K	EXP A TXN11C
EXP A TXP12	C68	0.22u/4/X5R/6.3V/K	EXP A TXP12C
EXP A TXN12	C70	0.22u/4/X5R/6.3V/K	EXP A TXN12C
EXP A TXP13	C72	0.22u/4/X5R/6.3V/K	EXP A TXP13C
EXP A TXN13	C73	0.22u/4/X5R/6.3V/K	EXP A TXN13C
EXP A TXP14	C74	0.22u/4/X5R/6.3V/K	EXP A TXP14C
EXP A TXN14	C75	0.22u/4/X5R/6.3V/K	EXP A TXN14C
EXP A TXP15	C77	0.22u/4/X5R/6.3V/K	EXP A TXP15C
EXP A TXN15	C78	0.22u/4/X5R/6.3V/K	EXP A TXN15C

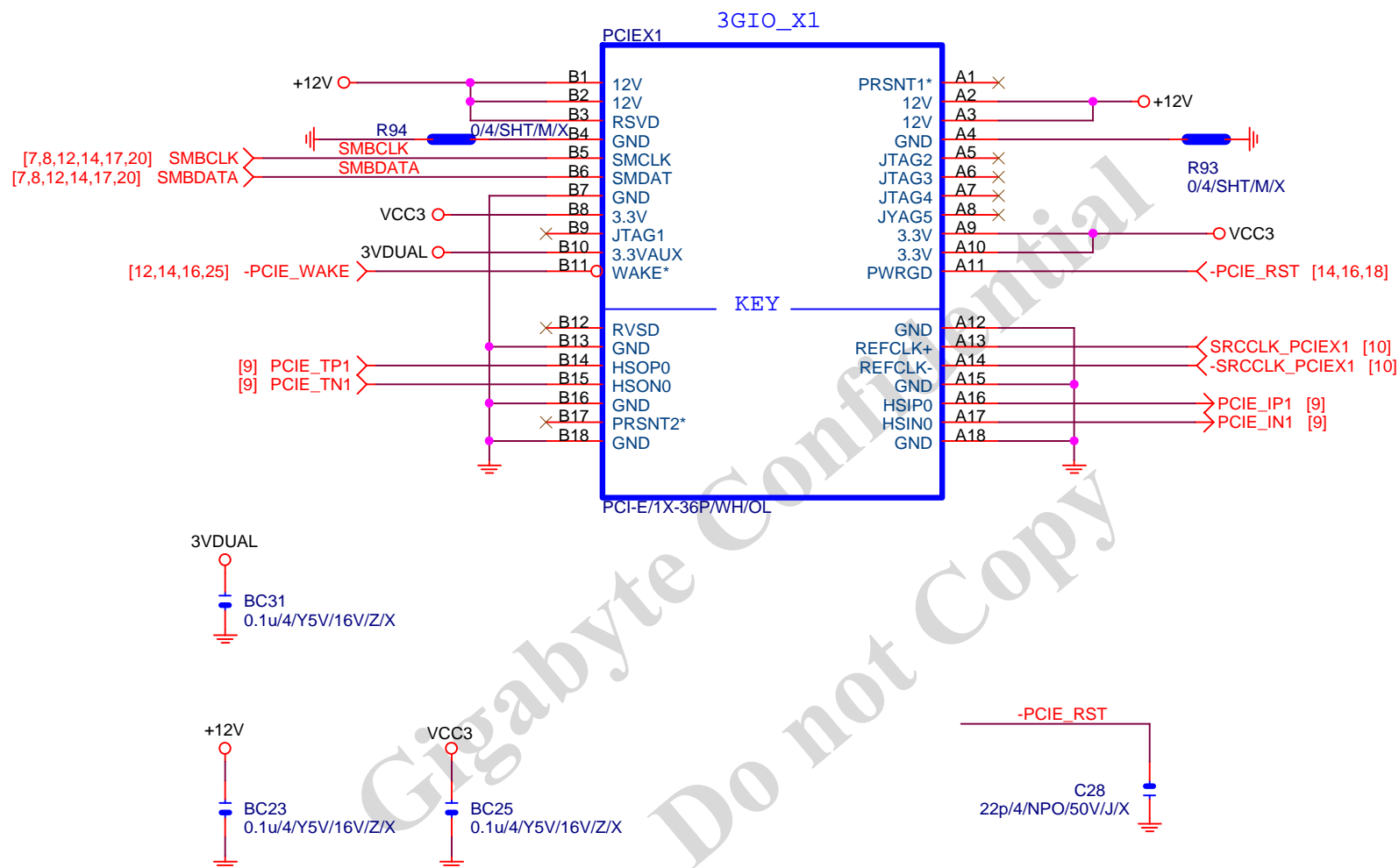


PCI-E/16X-164P/297C/LONG DOUBLE

LONG DOUBLE PUSH LATCH

Gigabyte Technology

Title			
PCI EXPRESS * 16			
Size			
Custom			
Document Number			
GA-H61M-S2PT			
Rev			
1.01			
Date:			
Friday, June 20, 2014			
Sheet			
14 of 32			

**Gigabyte Technology**

Title

PCI EXPRESS X 1 PORTSize
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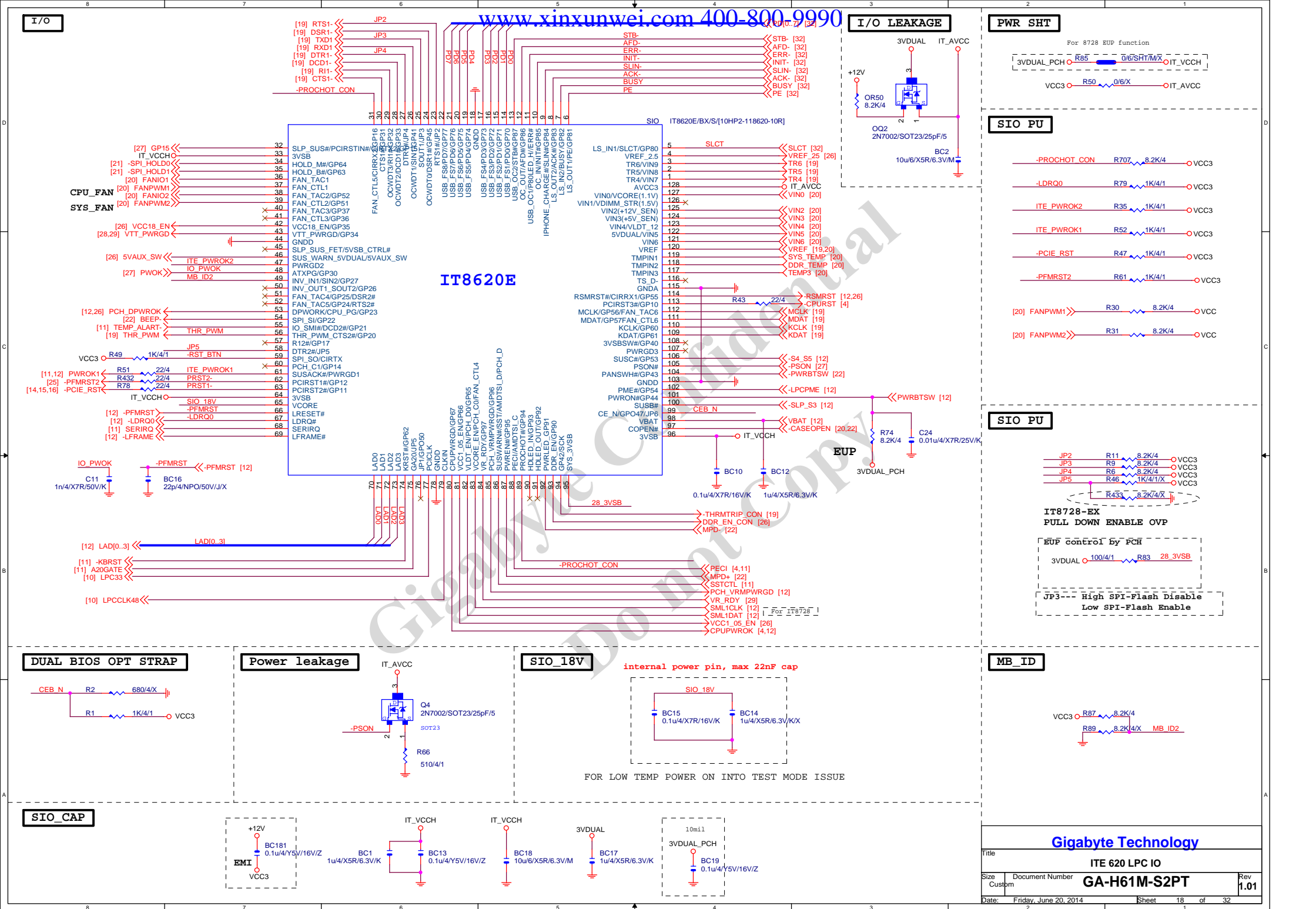
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GA-H61M-S2PTRev
1.01

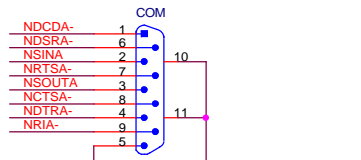
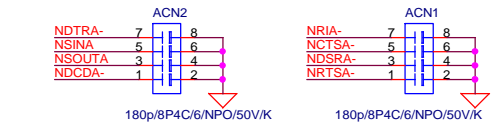
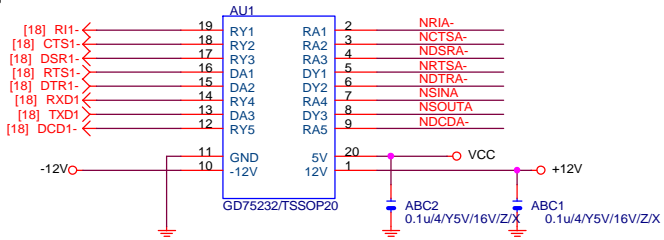
Date: Friday, June 20, 2014

Sheet 15 of 32

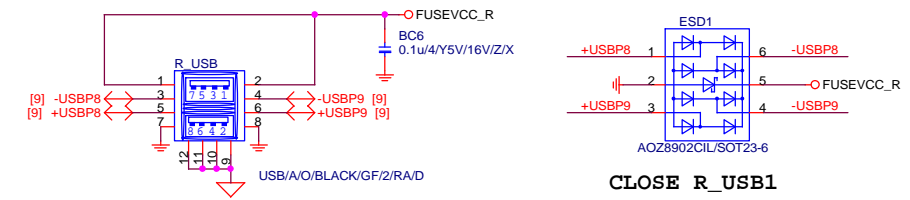
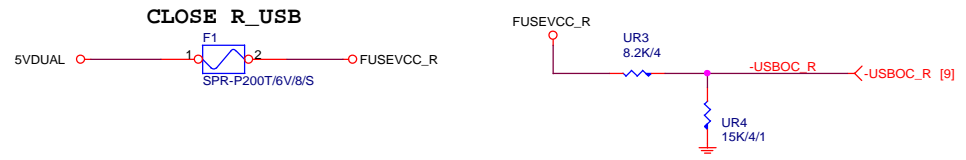
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Title			
ITE IT8892E			
Size	Document Number	GA-H61M-S2PT	Rev
Custom			1.0
Date:	Friday, June 20, 2014	Sheet	16 of 32



COM

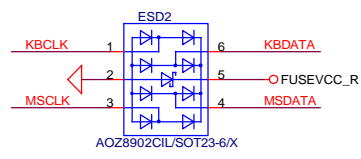
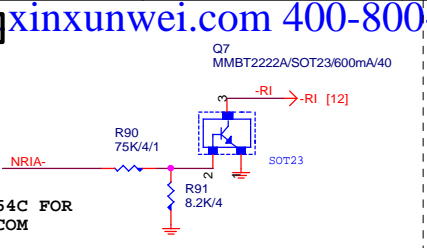


R_USB



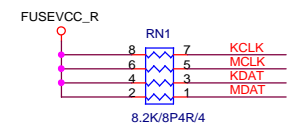
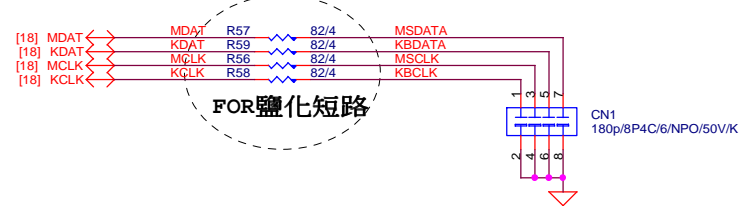
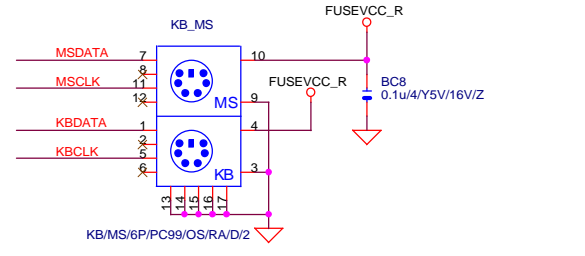
CLOSE R_USB1

COM_VIA

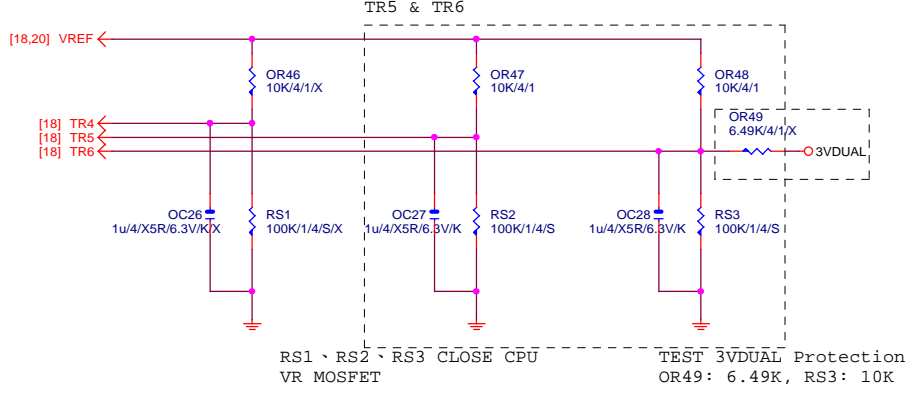


CLOSE KB_MS

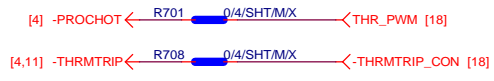
KB_MS



THERMAL SENSOR

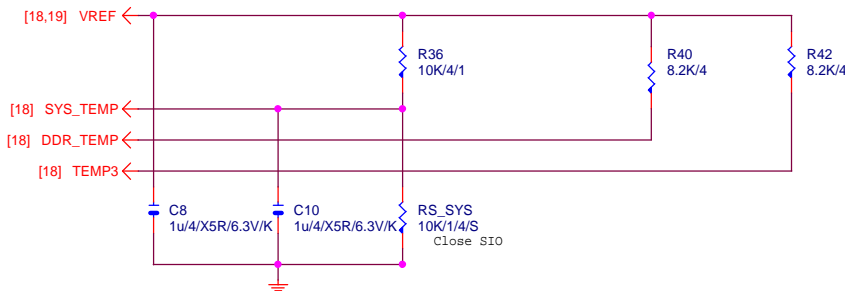


-PROHOT & THERMALTRIP

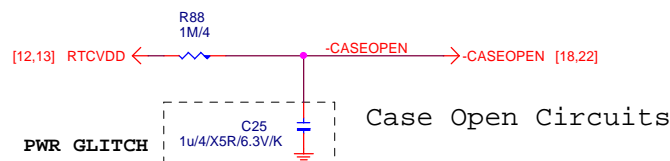


Gigabyte Technology			
Title			
COM,-RI,KB_USB,USB_ESATA,-PROHOT			
Size	Document Number	GA-H61M-S2PT	
Custom			Rev 1.01
Date:	Friday, June 20, 2014	Sheet	19 of 32

TEMP H/W MONITOR

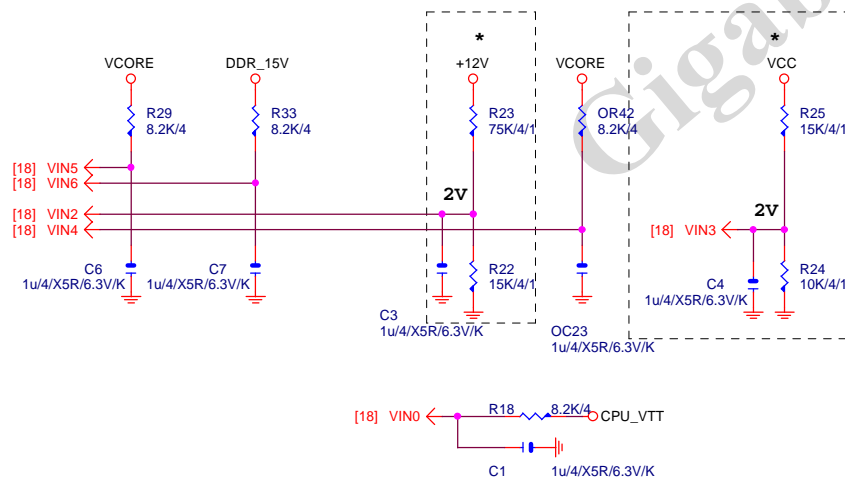


CASE OPEN

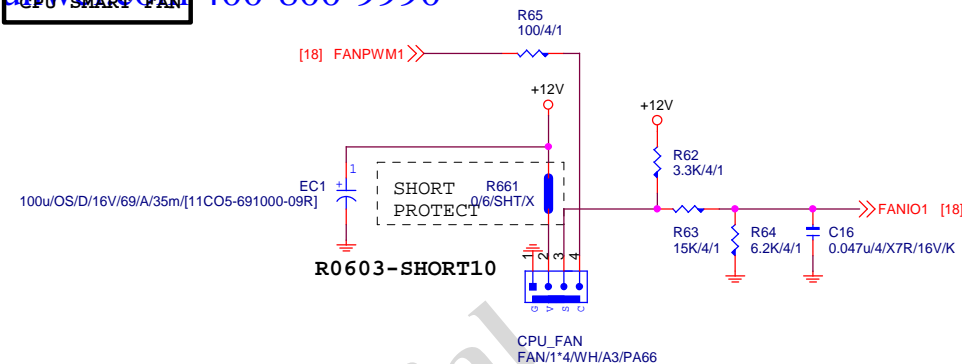


VOLTAGE-- H/W MONITOR

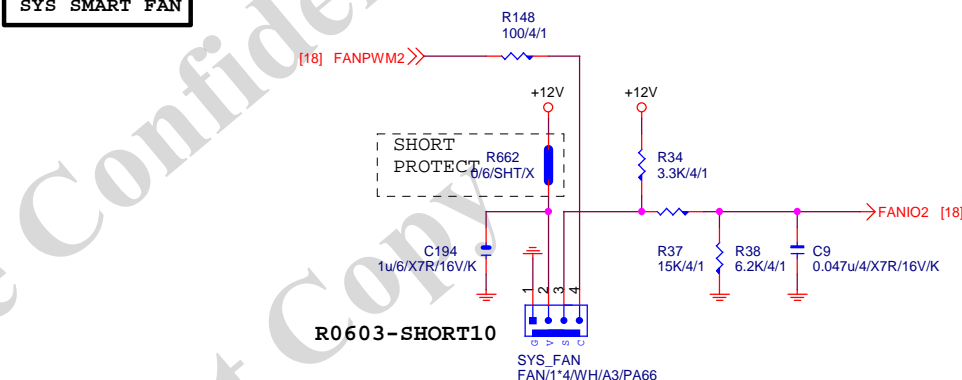
IT8728/EX VIN2/VIN3-->2V



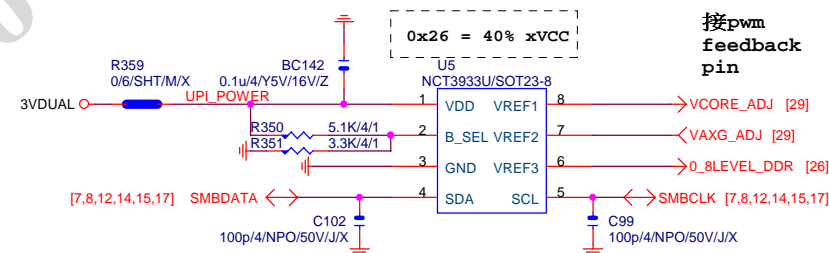
CPU SMART FAN



SYS SMART FAN



O.V.



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Title		HWM,FAN CTRL,OV	
Size	Document Number	GA-H61M-S2PT	
Custom			Rev 1.01
Date:	Friday, June 20, 2014	Sheet 20	of 32

H61使用32M BIOS

BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

1	means	floating
0	means	PD 1K

Gigabyte Technology

Title

DUAL BIOS

Size
A

Document Number

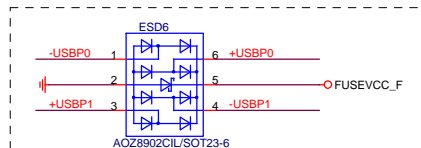
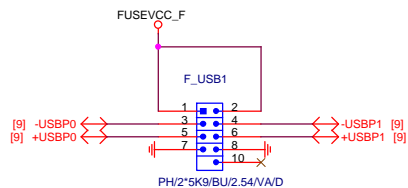
GA-H61M-S2PT

Rev
1.01

Date: Friday, June 20, 2014

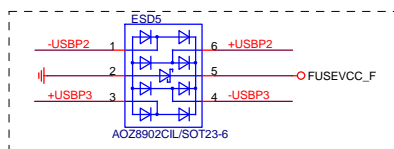
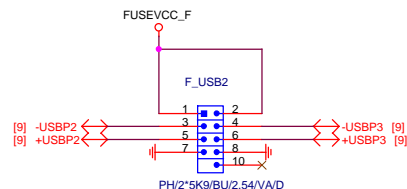
Sheet 21 of 32

FRONT USB1



Close to connector

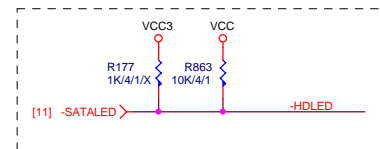
FRONT USB2



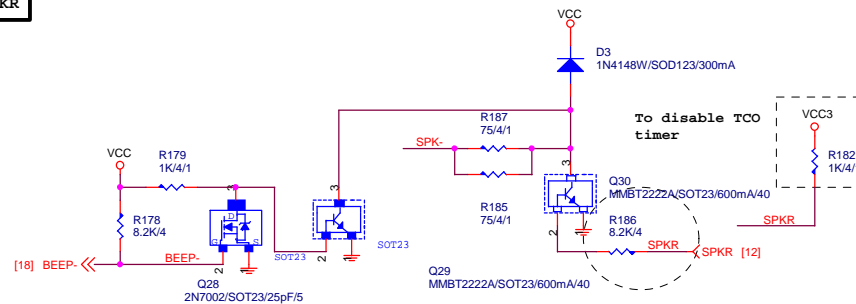
Close to connector

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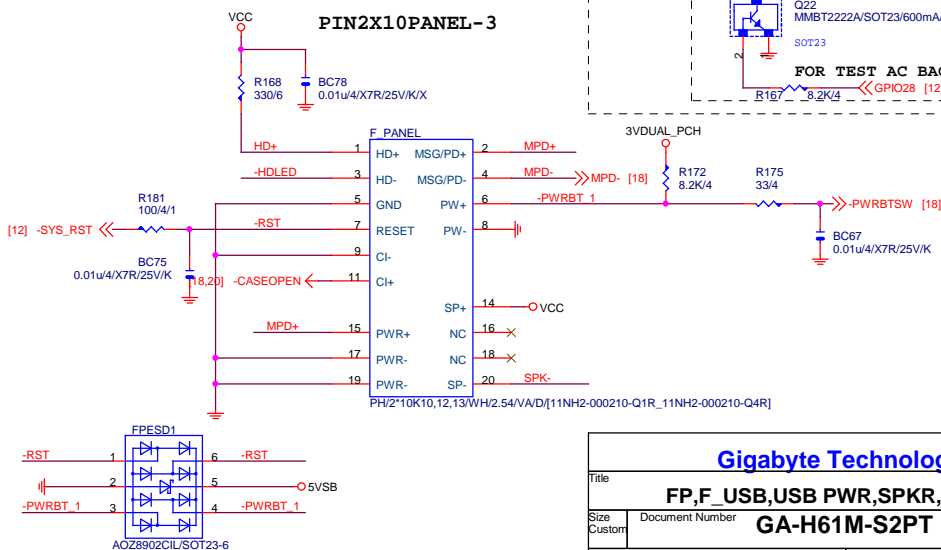
SPK1 LED



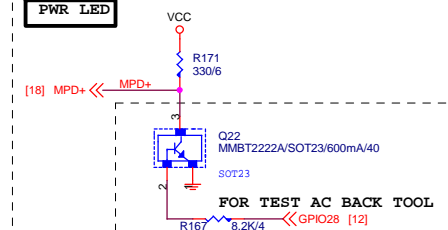
SPKR



INTEL FRONT PANEL



PWR LED



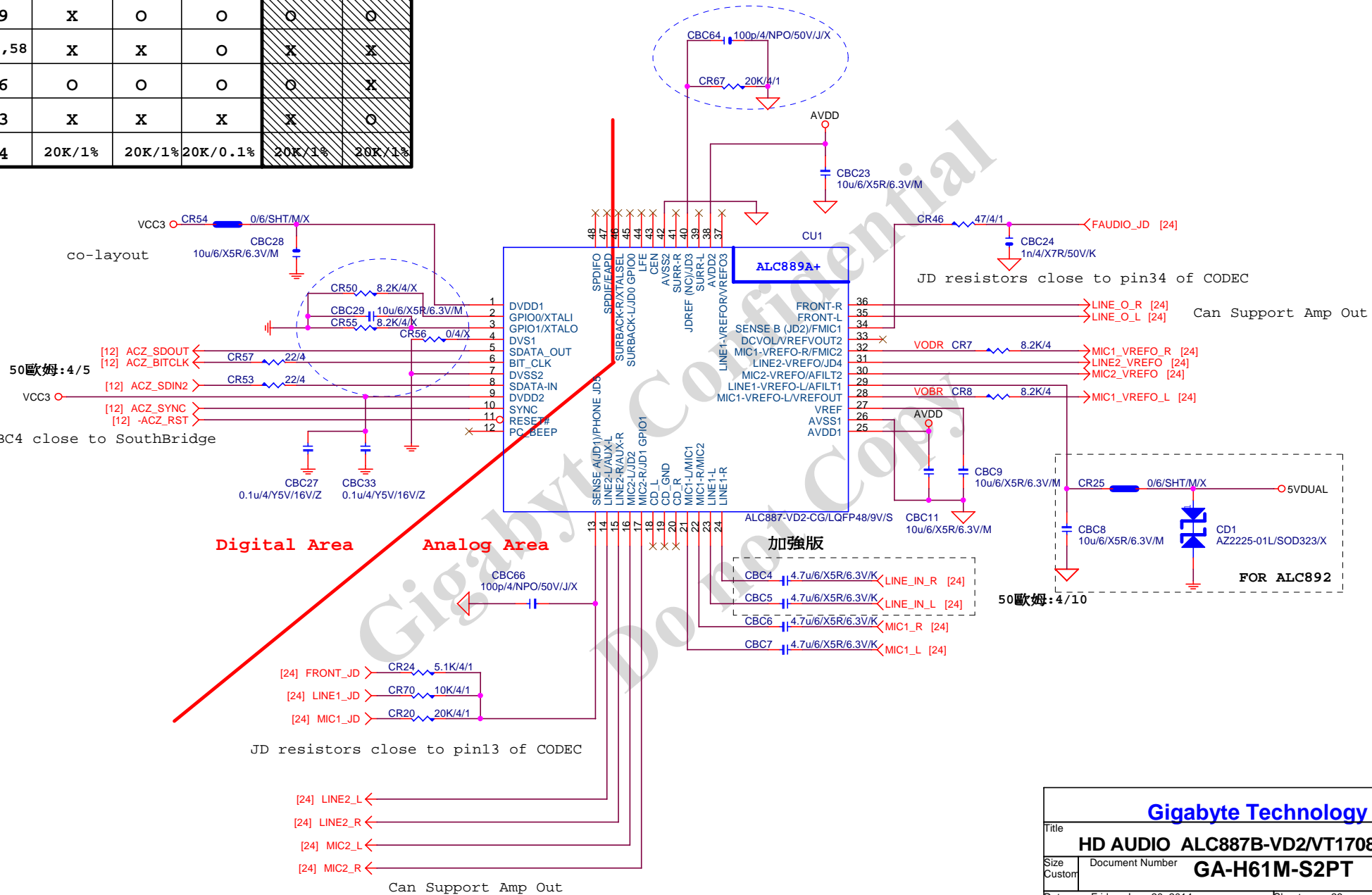
Gigabyte Technology

Title			
FP,F_USB,USB PWR,SPKR,SATA LED			
Size	Document Number	Rev	
Custom	GA-H61M-S2PT	1.01	
Date:	Friday, June 20, 2014	Sheet	22 of 32

AZALIA CODEC

ALC892/ALC889A/ALC889/ALC888B Colay

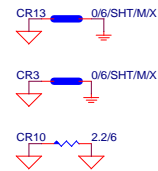
	ALC888B	ALC888 -VA	ALC889A	ALC888 -VD	ALC892
CR59	X	O	O	O	O
CR53,58	X	X	O	X	X
CR56	O	O	O	O	X
CR63	X	X	X	X	O
CR34	20K/1%	20K/1%	20K/0.1%	20K/1%	20K/1%



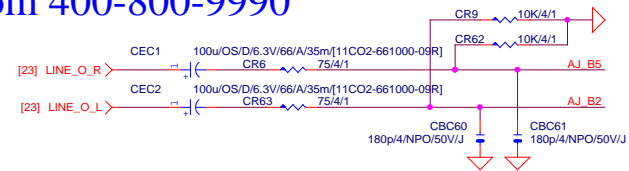
Gigabyte Technology

Title	HD AUDIO ALC887B-VD2/VT1708S/VT2021		
Size	Document Number	GA-H61M-S2PT	Rev
Custom			1.01
Date:	Friday, June 20, 2014	Sheet	23 of 32

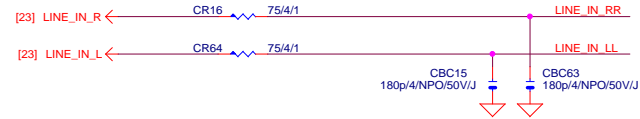
CODEC POWER/EMI PAD



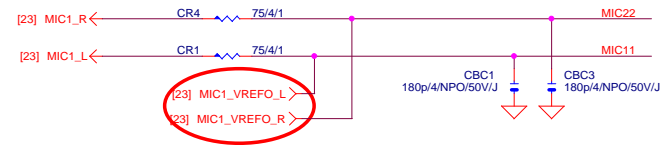
LINE-OUT



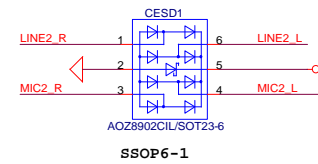
LINE-IN



MIC-IN



F_AUDIOESD

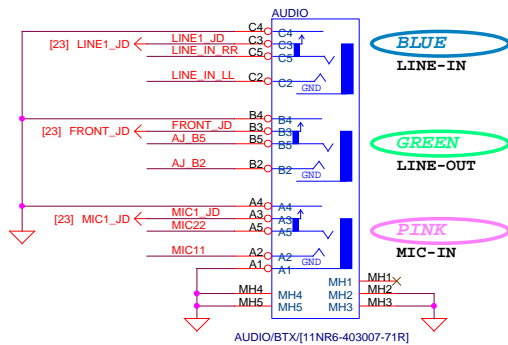


ALC892: X
ALC887: O

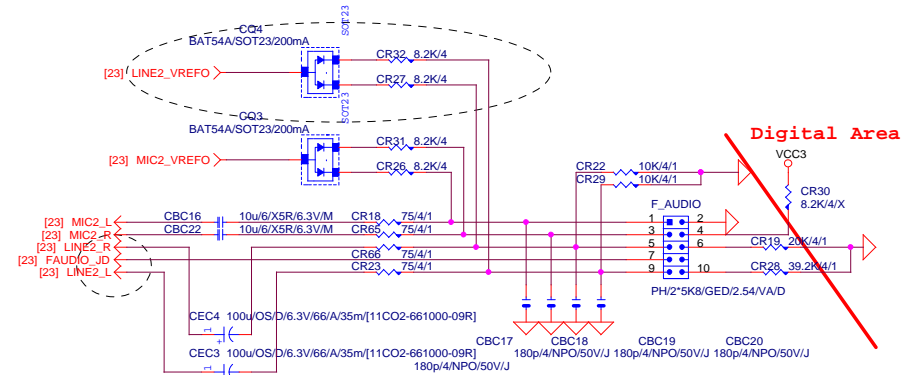
SPDIF

N/A

AZALIA JACK

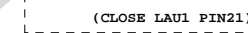
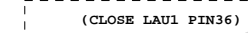
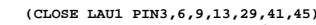
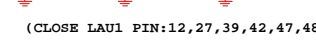
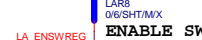


AZALIA FRONT PANEL

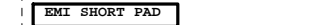
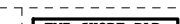
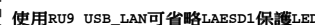


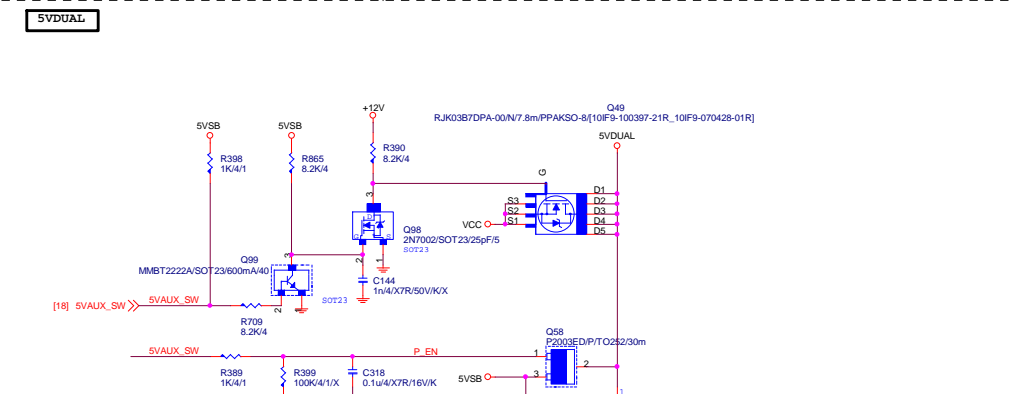
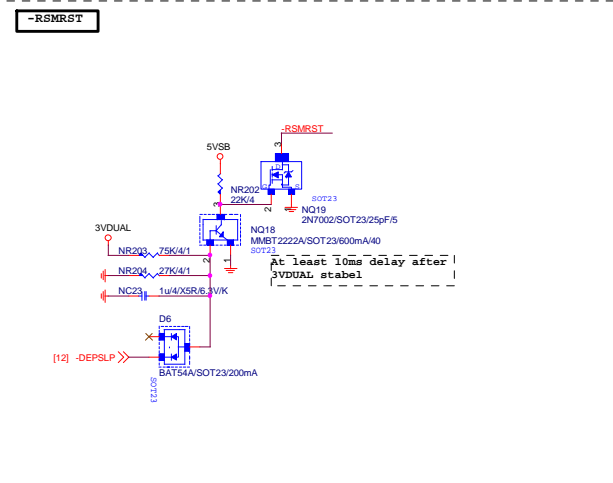
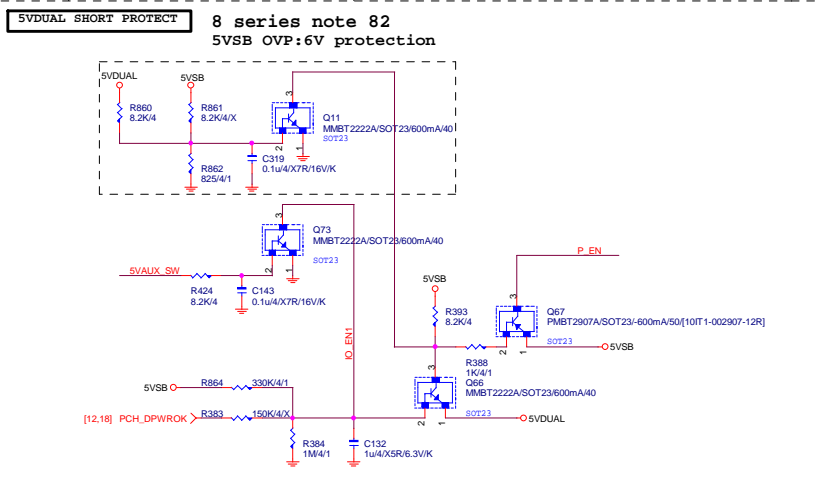
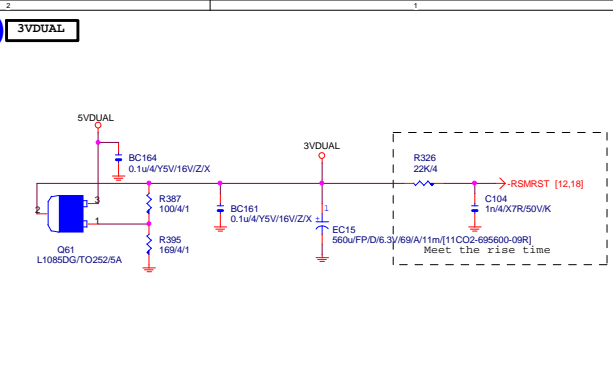
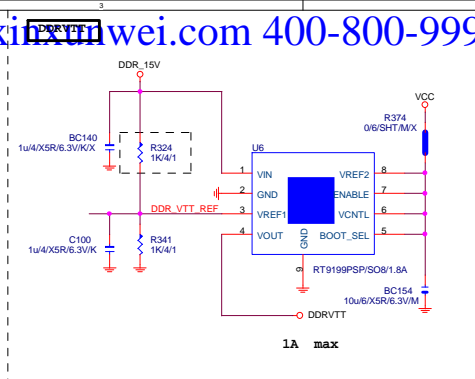
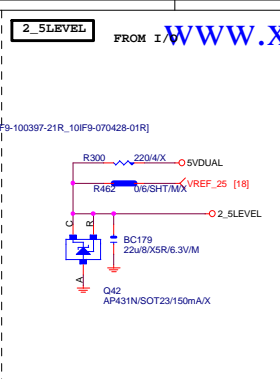
Gigabyte Technology

Title			AUDIO JACK
Size			GA-H61M-S2PT
Date:			Friday, June 20, 2014
Sheet			24 of 32
Rev			1.01



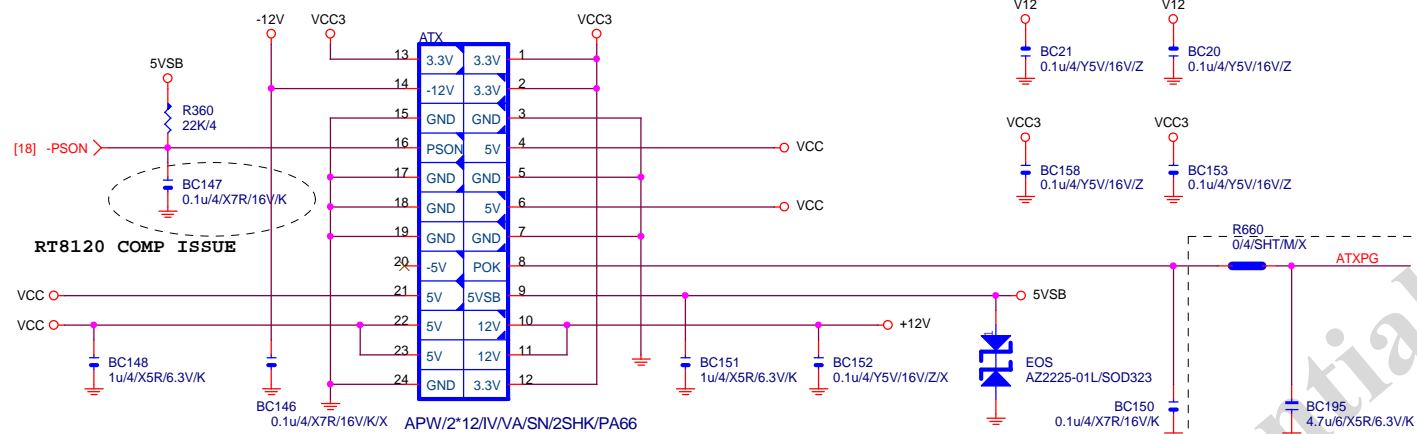
Power domain chart



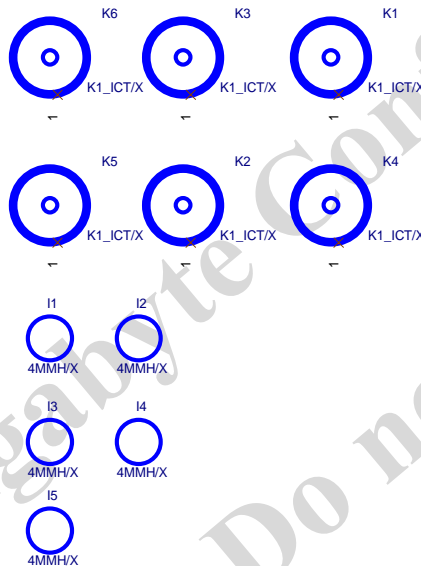
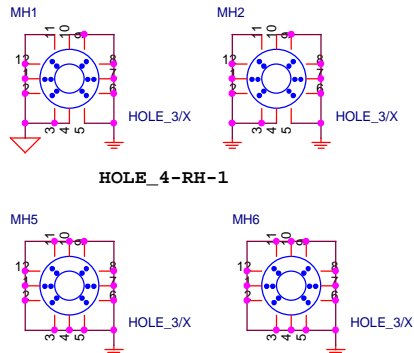


ATXX24 POWER CONNECTOR

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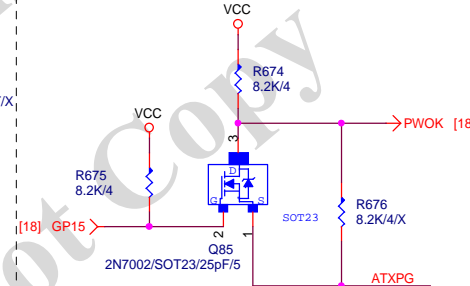


To prevent the 5VSB under loading when boot

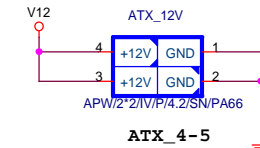


PWOK PATCH

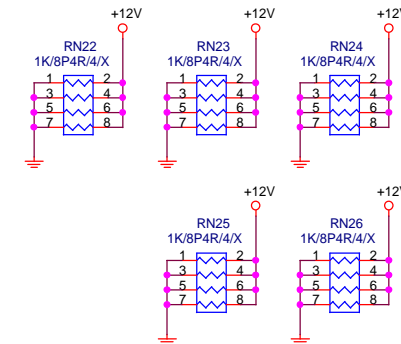
【技術通報R&D技術通報154】



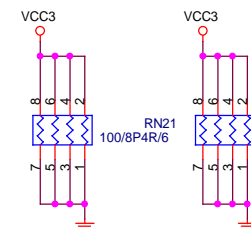
ATXX4 POWER CONNECTOR



MIN. LOAD



FIX PWR MINMUN LOAD



Gigabyte Technology

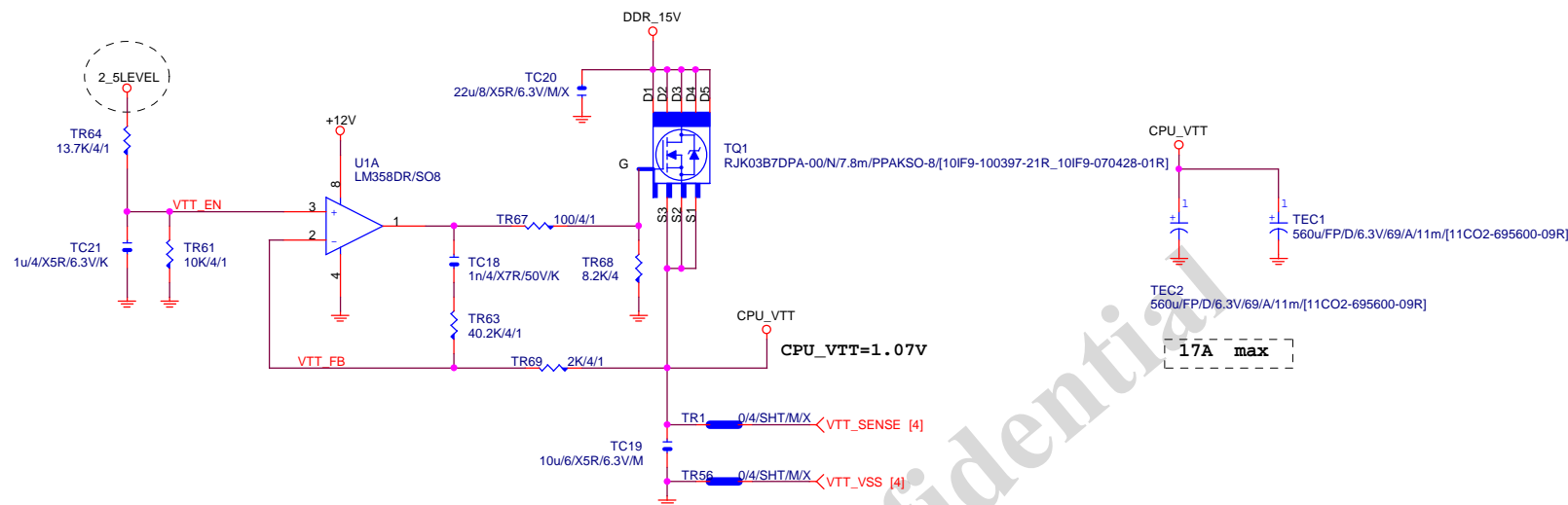
ATX CONNECTOR

GA-H61M-S2PT

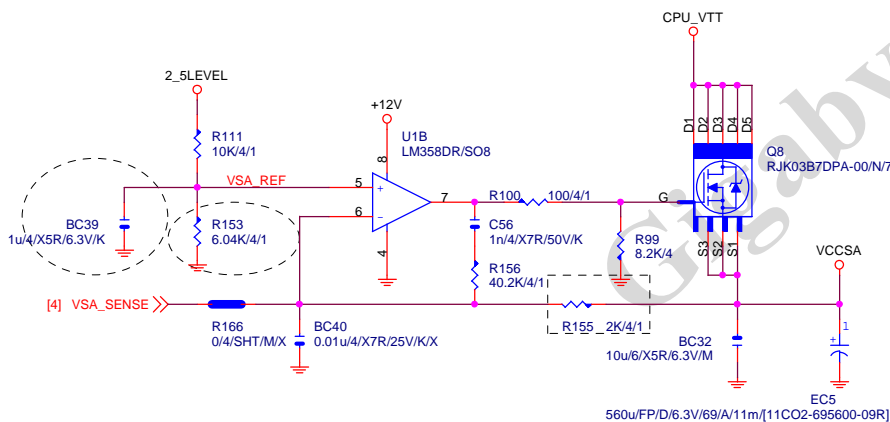
Title	Document Number	Rev
Size Custom	2	1.01
Date: Friday, June 20, 2014	Sheet 27 of 32	

CPU_VTT

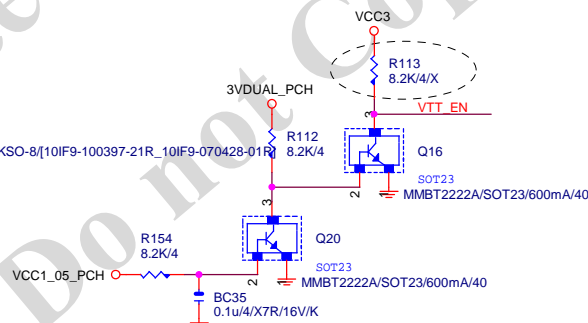
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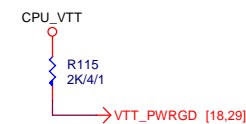
VCCSA



CPU_VTT PWR SEQ

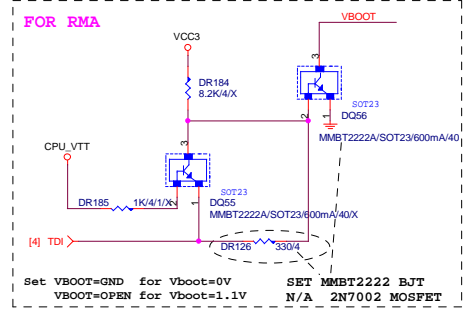


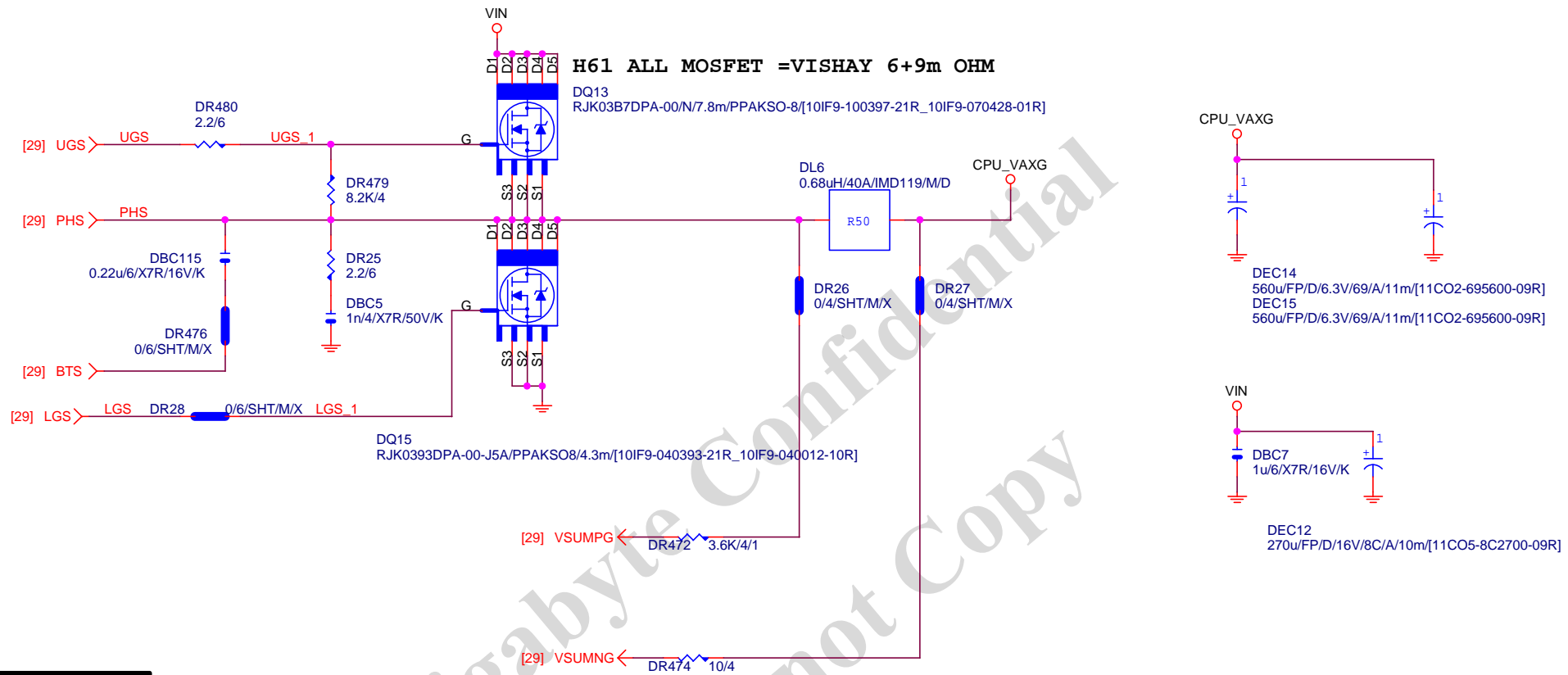
VTT_PWRGD



GIGABYTE

Title			
CPU_VTT			
Size	Document Number	Rev	
B	GA-H61M-S2PT	1.01	
Date:	Friday, June 20, 2014	Sheet	28 of 32



VAXG**MOS HEATSINK**

N/A

Gigabyte Technology

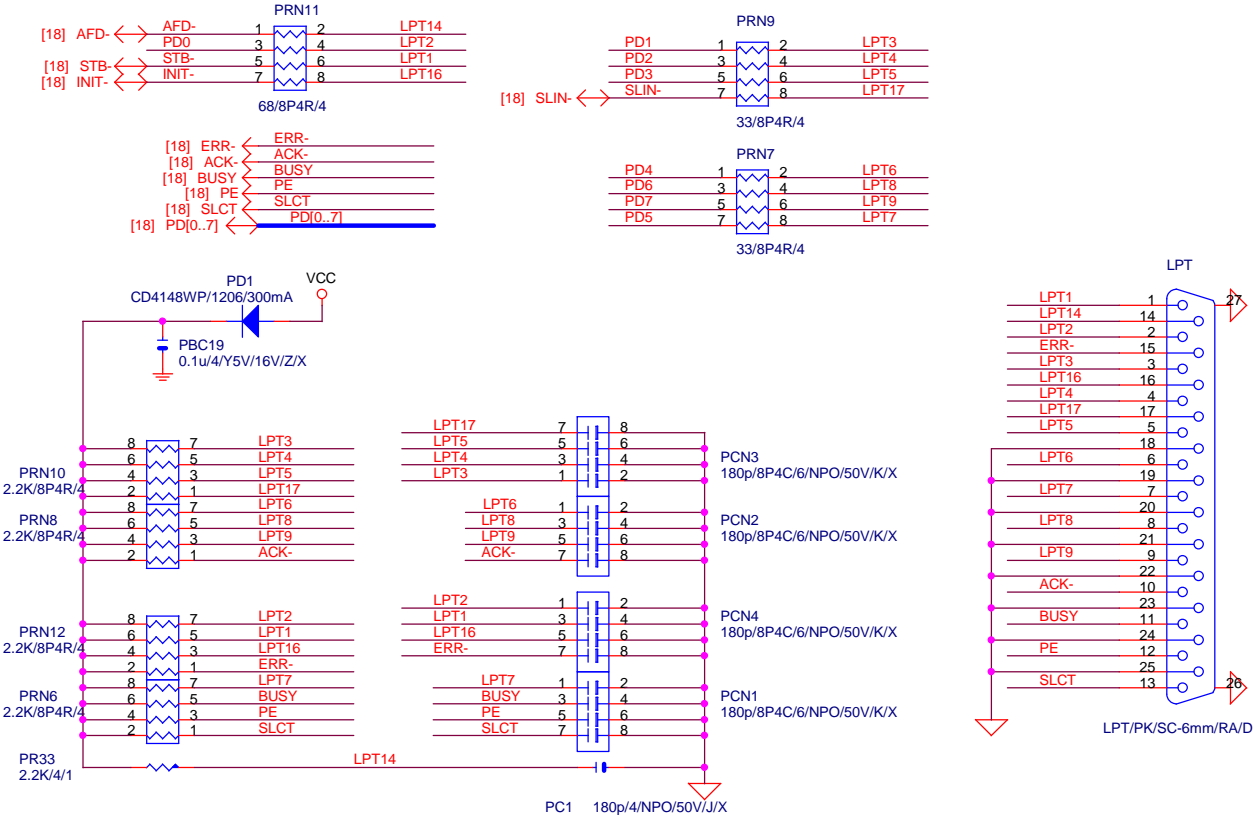
Title			
CPU CORE VR-2			
Size	Document Number	Rev	
Custom	GA-H61M-S2PT	1.01	
Date:	Friday, June 20, 2014	Sheet	30 of 32

TPM

N/A

LPT PORT

【技術通報R&D技術通報151】
33ohm Change to 68ohm



COMB

N/A

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LPT

GA-H61M-S2PT

Rev 1.01